

CV52S

4K AI Vision Processor

Key Features

Flexible Low-Power Platform

- CVflow[®] computer vision engine
- 64-bit dual-core Arm[®] Cortex[®]-A76 CPU up to 1.6 GHz
- Linux kernel version 5.10 or later (64-bit)

High-Efficiency Video Encoding

- H.265 and H.264 video compression
- Flexible multi-streaming capability
- 4KP60 with multiple substreams video performance
- Multiple CBR and VBR bit rate control modes
- Smart H.264 and H.265 encoder algorithms

Computer Vision Engine

- CNN / DNN-based processing: detection, classification, and more
- Accelerators for conventional CV operations
- CNN toolkit for easy porting of neural networks implemented in Caffe, TensorFlow, PyTorch, or ONNX frameworks

Advanced Image Processing

- Multi-exposure line-interleaved HDR
- Hardware dewarping engine
- Electronic image stabilization (EIS)
- Multiple camera support
- 3D motion-compensated temporal filtering (MCTF)
- Superior low-light processing
- RGGB / RCCB / RCCC / RGB-IR / monochrome sensor support

Target Applications

- 4K IP cameras with advanced artificial intelligence (AI)
- Intelligent traffic systems (ITS) cameras
- Machine vision and robotics applications



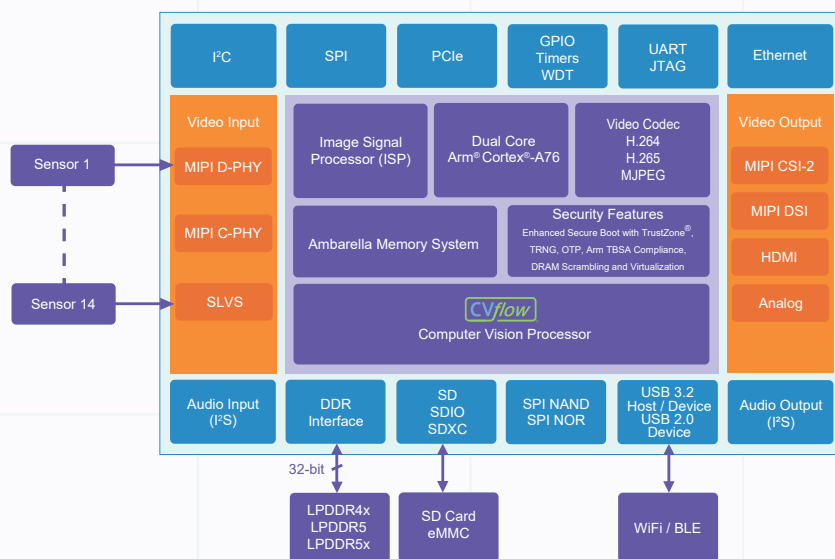
Overview

Ambarella's CV52S provides 4K image processing, video encoding / decoding, and CVflow[®] computer vision processing in a single, low-power design. Fabricated in advanced 5 nm process technology, CV52S achieves power consumption below 3W for 4KP60 video recording with advanced AI processing at 30 fps. CV52S's CVflow architecture provides deep neural network (DNN) multi-processing required for the next generation of intelligent cameras.

CV52S's advanced image signal processor (ISP) provides outstanding imaging in low-light conditions, while high dynamic range (HDR) processing extracts maximum image detail in high-contrast scenes, further enhancing the computer vision capabilities of the chip. CV52S includes efficient 4K encoding in both AVC and HEVC video formats, delivering high-resolution video recording and streaming with very low bitrates.

The CV52S's CVflow architecture enables computer vision processing at full 4K, enabling image recognition over long distances, with high accuracy. The CVflow engine can efficiently run multiple neural networks (NN) in parallel while accelerating classical computer vision algorithms, providing powerful computer vision acceleration at minimal power consumption. To help customers easily port their own neural networks onto the CV52S SoC, Ambarella's software development kit offers a complete set of tools for software and AI implementation.

CV52S includes a suite of advanced security features to implement on-device physical security, including enhanced secure boot with TrustZone[®] and secure memory, true random number generator (TRNG), one-time programmable memory (OTP), Arm trusted base system architecture (TBSA) compliance, DRAM scrambling, and virtualization.



CV52S Block Diagram

General Specifications

Processor Cores

- Dual-core Arm® Cortex®-A76 up to 1.6 GHz
- 64 KB / 64 KB L1 cache, 256 KB L2 cache, and 1024 KB L3 cache
- NEON™ SIMD and FPU acceleration
- AES / SHA-2 / ED25519 crypto acceleration

Computer Vision Processor

- CVflow processor with parallel architecture to boost performance of the low-level portion of perception algorithms

Video Input

- 2x MIPI D-PHY / MIPI C-PHY
 - Each MIPI D-PHY / C-PHY supports C-PHY mode (1–3 lanes) or D-PHY mode (1–4 lanes)
- 2x MIPI D-PHY (1–4 lanes each)
- 2x SLVS (1–4 lanes each)
- Up to 14 cameras using MIPI virtual channels

Video Output

- HDMI® 2.0 including PHY with CEC support
- PAL / NTSC composite SD video
- 2x MIPI DSI / CSI-2

CMOS Sensor / Image Processing

- Processing up to 720 Mpixel/s
- Lens shading, fixed-pattern noise correction
- Multi-exposure HDR (line-interleaved sensors)
- 3D motion-compensated temporal filtering (MCTF)

- RGG / RCCB / RCCC / RGB-IR / monochrome sensor support
- Adjustable AE / AWB / AF
- LED flicker compensation for LED sources
- Advanced dynamic range (WDR and HDR) engine
- Chromatic aberration correction
- 180° and 360° fisheye lens and geometric distortion correction
- OSD engine and overlays
- Gamma compensation and color enhancement
- Vignetting compensation
- 3-axis electronic image stabilization (EIS)
- Crop, mirror, flip, and 90° / 270° rotation

Video Encoding / Decoding

- H.265 (HEVC) MP L6.1, H.264 (AVC) MP / HP L6.1, and MJPEG
- Maximum encoding / decoding performance up to 4KP60 with multiple substreams
- Flexible GOP configuration with I, P, and B frames
- Multiple CBR and VBR rate control modules

Security Features

- Enhanced secure boot with TrustZone® and secure memory, true random number generator (TRNG), one-time programmable memory (OTP), Arm trusted base system architecture (TBSA) compliance, DRAM scrambling, and virtualization

Tools for Development

- CNN toolkit to ease the porting of CNNs trained using frameworks such as Caffe, PyTorch, TensorFlow, or ONNX
- Compiler, debugger, and profiler for both Arm and microcode development

Memory Interfaces

- LPDDR4x up to 4 Gbits/s / LPDDR5(x) up to 5 Gbits/s, 32-bit data bus, up to 16 GB capacity
- Three SD controllers
- Single- / dual- / quad- / octal-SPI NOR and single- / dual- / quad-SPI NAND
- Boot from SPI NAND / SPI NOR / USB / eMMC

Peripheral Interfaces

- 10 / 100 / 1000 Ethernet with RMII / RGMII
- 4-lane PCIe
- 1x USB 3.2 host / device and 1x USB 2.0 device only with PHY
- 2x I²S input and output interfaces, 1x DMIC
- 2x CAN FD interface
- Multiplexed 5x UART and 6 I/F of SSI / IDC
- Multiple GPIO ports, PWM, IR, and ADC
- Watchdog timer, general purpose timers, and JTAG

Physical

- 5 nm low-power CMOS technology
- 13 mm x 13 mm FC TFBGA with 0.5 mm ball pitch
- Operating temperature -25°C to +85°C

CV52S Camera Development Platform

The CV52S camera development platform contains the necessary tools, software, hardware, and documentation to develop a camera utilizing the powerful CVflow processor while supporting the development of customized features.

Evaluation Kit

- CV52S main board with connectors for sensor / lens board and peripherals
- Sensor board: Sony, ON Semi, Omnivision, and others
- Datasheet, BOM, schematics, and layout
- SDK and reference application with C source code

Software Development Kit

- Royalty-free libraries for ISP, dewarp, and video recording
- Image tuning and manufacturing calibration tools
- Detailed documentation, including a programmer's guide and more
- CNN / DNN model preparation, porting, and profiling tools

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