

CV25A

Automotive Computer Vision SoC

Key Features

Computer Vision Engine CVflow®

- Convolutional neural network (CNN)- / deep neural network (DNN)-based processing to enable detection, classification, tracking, and more
- Tools for high- and low-level algorithm development
- CNN toolkit for easy porting with Caffe, PyTorch, TensorFlow, and ONNX

Advanced Image Processing

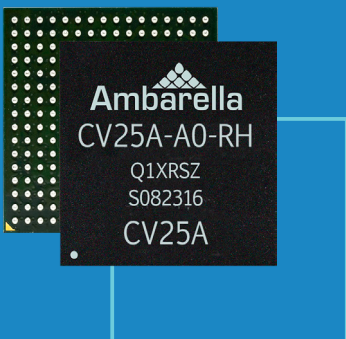
- Multi-exposure line-interleaved HDR
- Hardware dewarping engine support
- Electronic image stabilization (EIS)
- Multiple camera support
- 3D motion compensated temporal filtering (MCTF)
- Superior low-light processing
- RGGB / RCCB / RCCC / RGB-IR / monochrome sensor support

High-Efficiency Video Encoding

- H.265 and H.264 video compression
- Flexible multi-streaming capability
- 8MP30 video performance
- Multiple constant bit rate (CBR) and variable bit rate (VBR) control modes
- Smart H.264 and H.265 encoder algorithms

Target Applications

- Multi-channel drive recorders
- Aftermarket advanced driver assistance systems (ADAS)
- Single- / dual-channel electronic mirrors
- Driver / in-cabin monitoring systems (DMS / CMS)

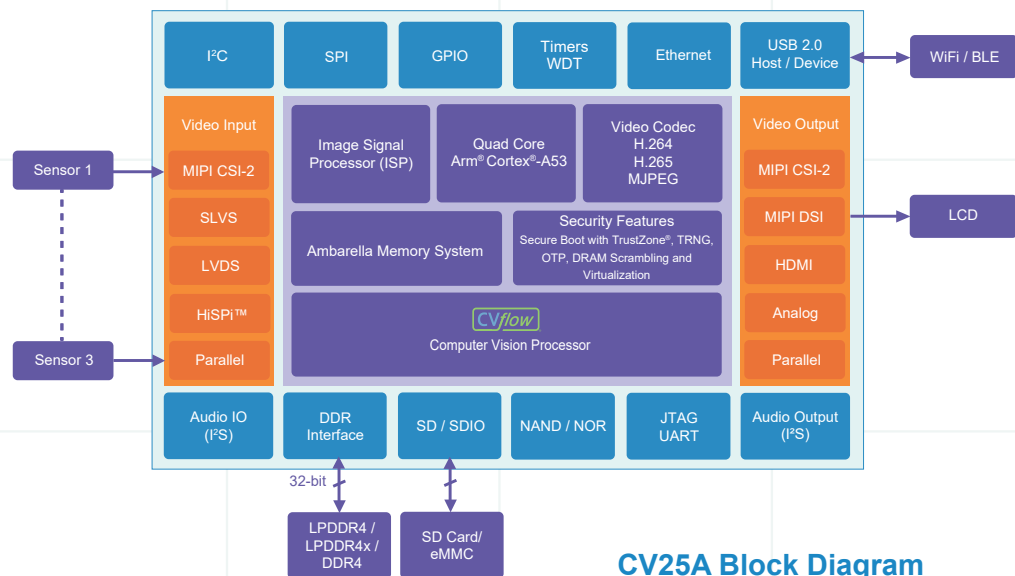


Overview

Ambarella's CV25A system on chip (SoC) combines image processing, 8MP30 video encoding / decoding, and CVflow® computer vision processing in a single, low-power design, making it an ideal choice to enable the next generation of intelligent automotive devices. The CV25A's CVflow architecture provides the DNN processing platform required for implementing computer vision algorithms for a wide range of use cases, including dash cameras, aftermarket ADAS devices, and more. Fabricated in advanced 10 nm process technology, it achieves an industry-leading combination of low power and high performance in both human vision and computer vision applications.

CV25A implements a highly efficient 8MP30 AVC (H.264) / HEVC (H.265) encoder in hardware along with an industry-leading image signal processor (ISP). The CV25A's ISP provides outstanding imaging in low-light conditions while high dynamic range (HDR) processing extracts maximum image detail in high-contrast scenes, further enhancing the computer vision capabilities of the chip. The flexible architecture allows encoding of multiple streams that are optimized for storage and video streaming over WiFi / BLE simultaneously. The chip also supports ultra wide angle and fisheye lenses by performing distortion correction of the video in hardware.

The CV25A's CVflow architecture provides computer vision processing at full 8MP resolution to enable image recognition over long distances and with high accuracy. The CVflow core allows the implementation of algorithms such as forward collision warning, lane departure warning, driver monitoring, and more. To help customers easily port their own neural networks onto the CV25A SoC, Ambarella's software development kit (SDK) offers a complete set of tools.



CV25A Block Diagram

General Specifications

Processor Cores

- Quad-core Arm®Cortex®-A53 up to 1 GHz
- 32 KB / 32 KB L1 cache, 1 MB L2 cache
- NEON™ SIMD and FPU acceleration
- AES / SHA1 / SHA2-256 crypto acceleration

Computer Vision Processor

- CVflow processor with parallel architecture to boost performance of the low-level portion of perception algorithms

Video Input

- Single, dual, or triple sensor inputs with independent ISP configuration
- LVDS / MIPI CSI-2® / SLVS / HiSPi™
- 16-bit parallel LVCMOS (BT. 601 / 656)

Video Output

- 16-bit parallel LVCMOS (BT. 601)
- HDMI® 2.0 including PHY with consumer electronic control (CEC) support
- PAL / NTSC composite SD video
- MIPI DSI / CSI-2 and FPD (VESA / JEIDA) out

CMOS Sensor / Image Processing

- 8MP30 maximum input resolution
- Lens shading and fixed-pattern noise correction
- Multi-exposure HDR (line-interleaved sensors)
- 3D motion-compensated temporal filtering (MCTF)
- RGBB / RCCB / RCCC / RGB-IR / monochrome sensor support

- Adjustable auto exposure (AE) / auto white balance (AWB)
- High dynamic range (HDR) engine
- Chromatic aberration correction
- 180° fisheye lens and geometric distortion correction
- On-screen display (OSD) engine and overlays
- Gamma compensation and color enhancement
- Vignetting compensation
- 3-axis electronic image stabilization (EIS)
- Crop, mirror, flip, 90° / 270° rotation

Video Encoding / Decoding

- H.265 (HEVC) MP L5.1, H.264 MP / HP L5.1, and MJPEG
- 8MP30 maximum encoding / decoding performance
- Flexible group of pictures (GOP) configuration with I, P, and B frames
- Multiple CBR and VBR control modules

Security Features

- Secure boot with TrustZone®, TRNG, OTP, DRAM scrambling and virtualization

Tools for Development

- CNN toolkit to ease the porting of CNNs trained using frameworks such as Caffe, TensorFlow, or ONNX
- Compiler, debugger, and profiler for both Arm and microcode development

Memory Interfaces

- LPDDR4x / LPDDR4 / DDR4 up to 1.6 GHz clock rate, 32-bit data bus, up to 2 GB capacity
- Three SD controllers
- Boot from SPI / parallel SLC NAND with BCH / SPI NOR / USB / eMMC
- Single- / dual- / quad-SPI NOR and SPI NAND

Peripheral Interfaces

- 10 / 100 / 1000 Ethernet AVB with RMII / RGMII
- One USB 2.0 port configurable as device / host with PHY
- Audio interface including I2S and DMIC
- Multiple SSI / SPI, IDC, and UART
- Multiple GPIO ports, PWM, IR, ADC
- Watchdog timer, general purpose timers, and JTAG

Physical

- 10 nm low-power complimentary metal-oxide semiconductor (CMOS)
- FC TFBGA package (361 balls, 13x13 mm, 0.65 mm pitch)
- Operating temperature -20°C to +85°C (additional operational temperature options available)

CV25A Camera Development Platform

The CV25A camera development platform contains the necessary tools, software, hardware, and documentation to develop a camera utilizing the powerful CVflow processor while supporting the development of customized features.

Evaluation Kit

- CV25A main board with connectors for sensor / lens board and peripherals
- Sensor board: Sony, onsemi, Omnivision, and others
- Datasheet, BOM, schematics, and layout
- SDK and reference application with C source code available with additional licenings

Software Development Kit

- Royalty-free libraries for ISP, dewarp, and video recording
- Image tuning and manufacturing calibration tools
- Detailed documentation, including a programmer's guide and more
- CNN / DNN model preparation, porting, and profiling tools

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